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caused by differences in scale

Chapter #3: Diodes

6 Chapter 7 Problem: 7.96 1. The schematic for this problem is shown below 2. The transistor used here has k n' = 71.2 µA/V2. So, W/L =14µ/0.5µ is chosen to

get k n= 2 mA/V2. 3. Simulate the netlist and find out the operating voltages. 4. The other operating parameters are 5.

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2 Chapter 12 4. The cross over interval is 2×2.9 µs=5.8 µs. So, it is 5.8 %. 5. Run the parametric analysis and sweep RL from 500Ω to 700Ω in steps of 50 Ω or smaller. Plot V(VO) as shown

below. 6. The output voltage is half of the input voltage when RL= 650 Ω. Netlist:

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