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Nanoscale CMOS Abstract: This paper examines the apparent limits, possible extensions, and applications of CMOS technology in the nanometer regime. Starting from device scaling theory and current industry projections, we analyze the achievable performance and possible limits of CMOS technology from the point of view of device physics, device technology, and power consumption.

Nanoscale CMOS - IEEE Journals & Magazine

Nanotechnology promises to open up new ways of scaling CMOS circuits by introducing new materials. For example, a hybrid circuit of CMOS gates and carbon

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Three-Dimensional Nanoscale Mapping of State-of-the-Art ...

Abstract: Steep subthreshold swing transistors based on interband tunneling are examined toward extending the performance of electronics systems. In particular, this review introduces and summarizes progress in the development of the tunnel field-effect transistors (TFETs) including its origin, current experimental and theoretical performance relative to the metal-oxide-semiconductor field ...

Low-Voltage Tunnel Transistors for Beyond CMOS Logic ...

ABSTRACT CMOS technology scaling has followed Moore's law well into the nanoscale regime now. The technology scaling is no longer just about geometric reduction but more about innovation in the use of new materials and transistor architectures.

Circuit Design in Nano-Scale CMOS Technologies ...

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better defining the likely capabilities of future microelectronic systems. ...  
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Scaling Rules for Three Cases (  $L$  Is the Dimensional Scaling Parameter,  $L$  Is the  
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ICCAD '05: Proceedings of the 2005 IEEE/ACM International conference on  
Computer-aided design FinFETs for nanoscale CMOS digital integrated circuits  
Pages 207-210

FinFETs for nanoscale CMOS digital integrated circuits ...  
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CMOS technology will continue to expand its dominance for the next decade or so  
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Abstract: As complementary metal-oxide-semiconductor (CMOS) technologies are scaled down into the nanometer range, a number of major nonidealities must be addressed and overcome to achieve a successful analog and physical circuit design. The nature of these nonidealities has been well reported in the technical literature.

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